

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-10. (Canceled)

11. (Currently Amended) An image signal decoding apparatus comprising:
a main storage division used for controlling the entire image signal decoding
apparatus, which has a frame storage division for storing frame data for performing the
motion compensation process; and
a decoding processing division for performing a decoding process including
motion compensation, which has a dedicated storage division used for a motion compensation
process in decoding of an image signal, and a motion compensation processing division for
performing the motion compensation process to the image signal, and
wherein said dedicated storage division stores, of the frame data stored in said
frame storage division, the frame data of a predetermined address highly likely to be referred
to in the motion compensation process; and
said motion compensation processing division performs the motion
compensation process by referring to the frame data stored in said dedicated storage division,
and
wherein said frame storage division stores the frame data of one frame and
overwrites the processing results outputted by said motion compensation processing division
to the corresponding address of the stored frame data, and

~~The image signal decoding apparatus according to claim 9,~~ wherein, in the case where said motion compensation processing division needs to refer to the frame data not stored in the frame storage division, said address administration division has a predetermined error compensation process that is defined performed.

12. (Currently Amended) An image signal decoding apparatus comprising:
a main storage division used for controlling the entire image signal decoding
apparatus, which has a frame storage division for storing frame data for performing the
motion compensation process; and
a decoding processing division for performing a decoding process including
motion compensation, which has a dedicated storage division used for a motion compensation
process in decoding of an image signal, and a motion compensation processing division for
performing the motion compensation process to the image signal, and
wherein said dedicated storage division stores, of the frame data stored in said
frame storage division, the frame data of a predetermined address highly likely to be referred
to in the motion compensation process; and
said motion compensation processing division performs the motion
compensation process by referring to the frame data stored in said dedicated storage division,
and
wherein said frame storage division stores the frame data of one frame and
overwrites the processing results outputted by said motion compensation processing division
to the corresponding address of the stored frame data, and
~~The image signal decoding apparatus according to claim 9, wherein said main~~
storage division stores DC (Direct Current) component data of the frame data of a forward
reference frame referred to for the motion compensation process, and in the case where said
motion compensation processing division needs to refer to the frame data stored in the frame
storage division, said address administration division has the frame data referred to, and has
the error compensation process performed by referring to said DC component data in the case
where said motion compensation processing division needs to refer to the frame data not
stored in the frame storage division.

13. (Currently Amended) An image signal decoding apparatus comprising:
a main storage division used for controlling the entire image signal decoding
apparatus, which has a frame storage division for storing frame data for performing the
motion compensation process; and
a decoding processing division for performing a decoding process including
motion compensation, which has a dedicated storage division used for a motion compensation
process in decoding of an image signal, and a motion compensation processing division for
performing the motion compensation process to the image signal, and
wherein said dedicated storage division stores, of the frame data stored in said
frame storage division, the frame data of a predetermined address highly like to be referred to
in the motion compensation process; and
said motion compensation processing division performs the motion
compensation process by referring to the frame data stored in said dedicated storage division,
and
wherein said frame storage division stores the frame data of one frame and
overwrites the processing results outputted by said motion compensation processing division
to the corresponding address of the stored frame data, and

~~The image signal decoding apparatus according to claim 9,~~ wherein said main storage division stores sub-sample data generated from the frame data of the forward reference frame referred for the motion compensation process, and in the case where said motion compensation processing division needs to refer to the frame data stored in the frame storage division, said address administration division has the frame data referred to, and has the error compensation process performed by referring to said sub-sample data in the case where said motion compensation processing division needs to refer to the frame data not stored in the frame storage division.